

Fig. 1

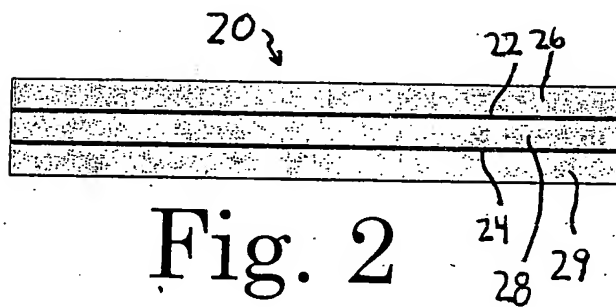


Fig. 2

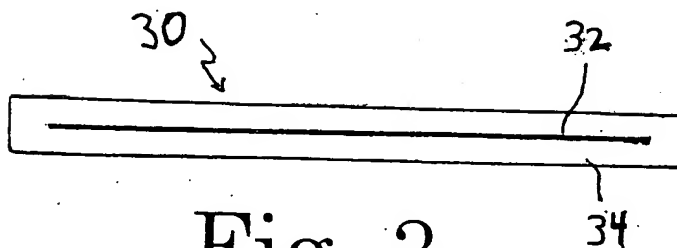


Fig. 3

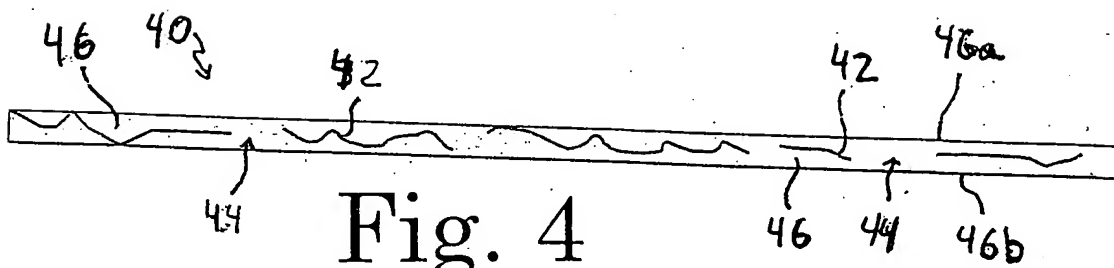


Fig. 4

Fig. 5

3 Layer SMC, Mode 2

—◆— SMC 34, control —■— SMC 36, MSC

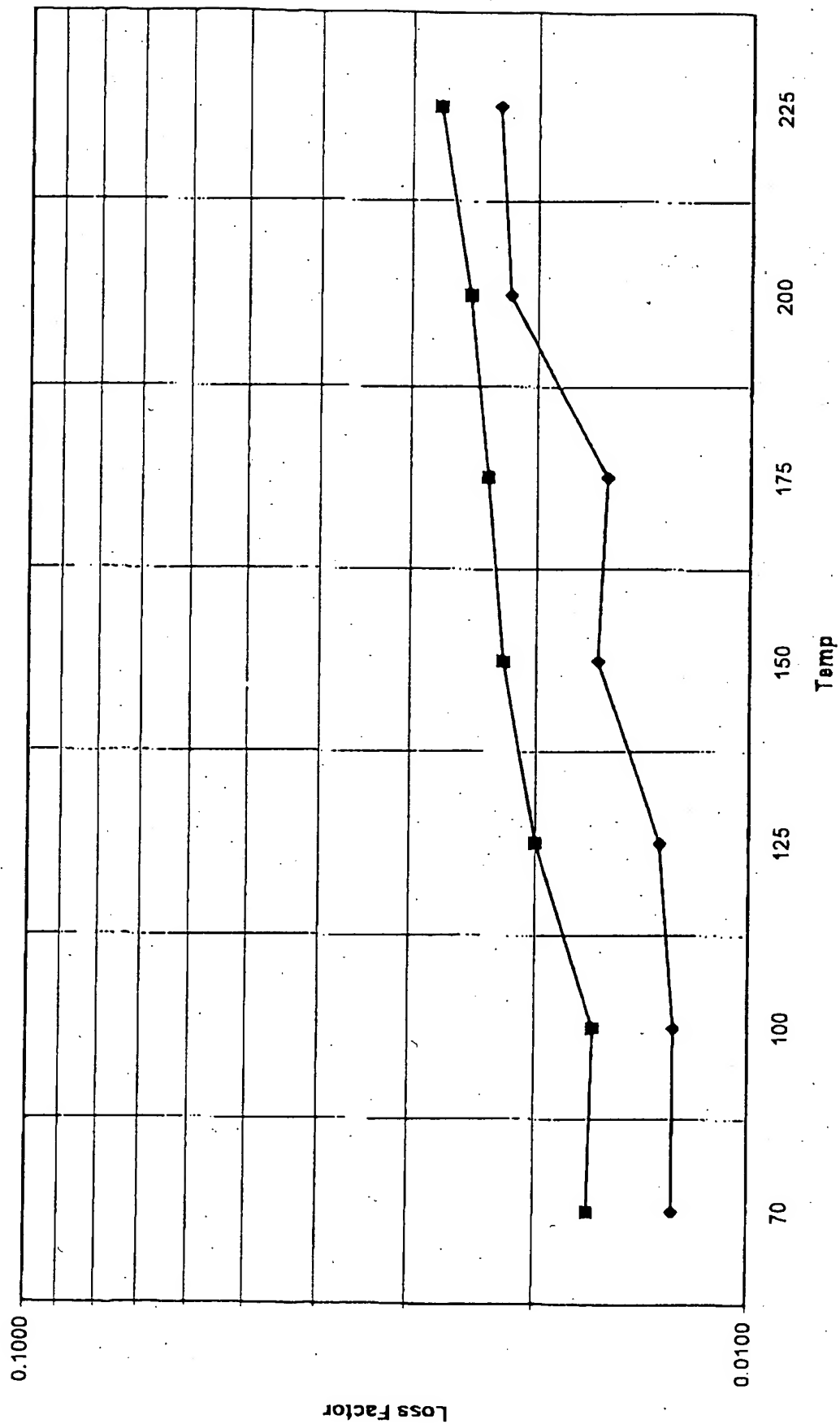


Fig. 6

3 Layer smc, Mode 3

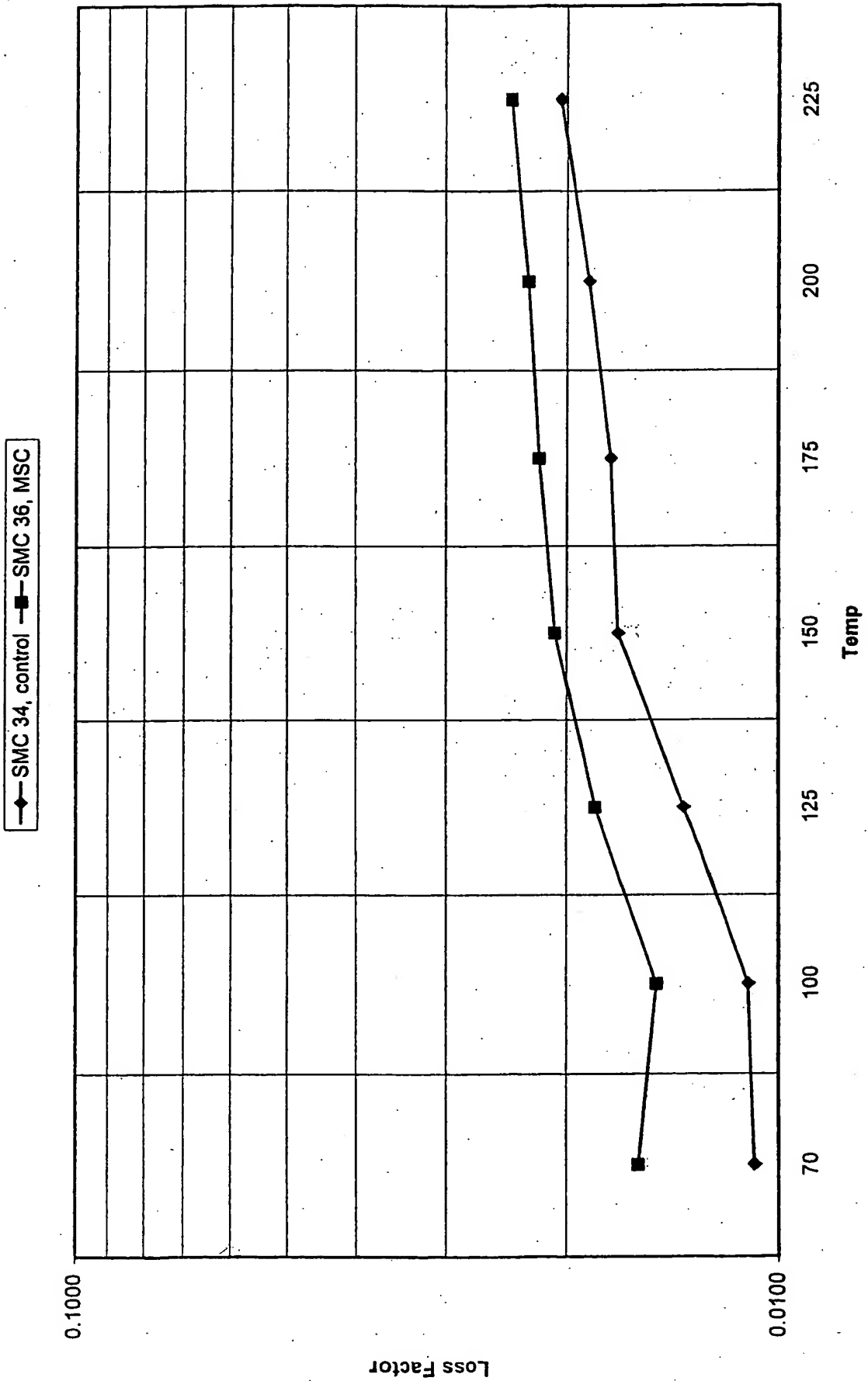


Fig. 7

Jelly rolled SMC, Mode 2

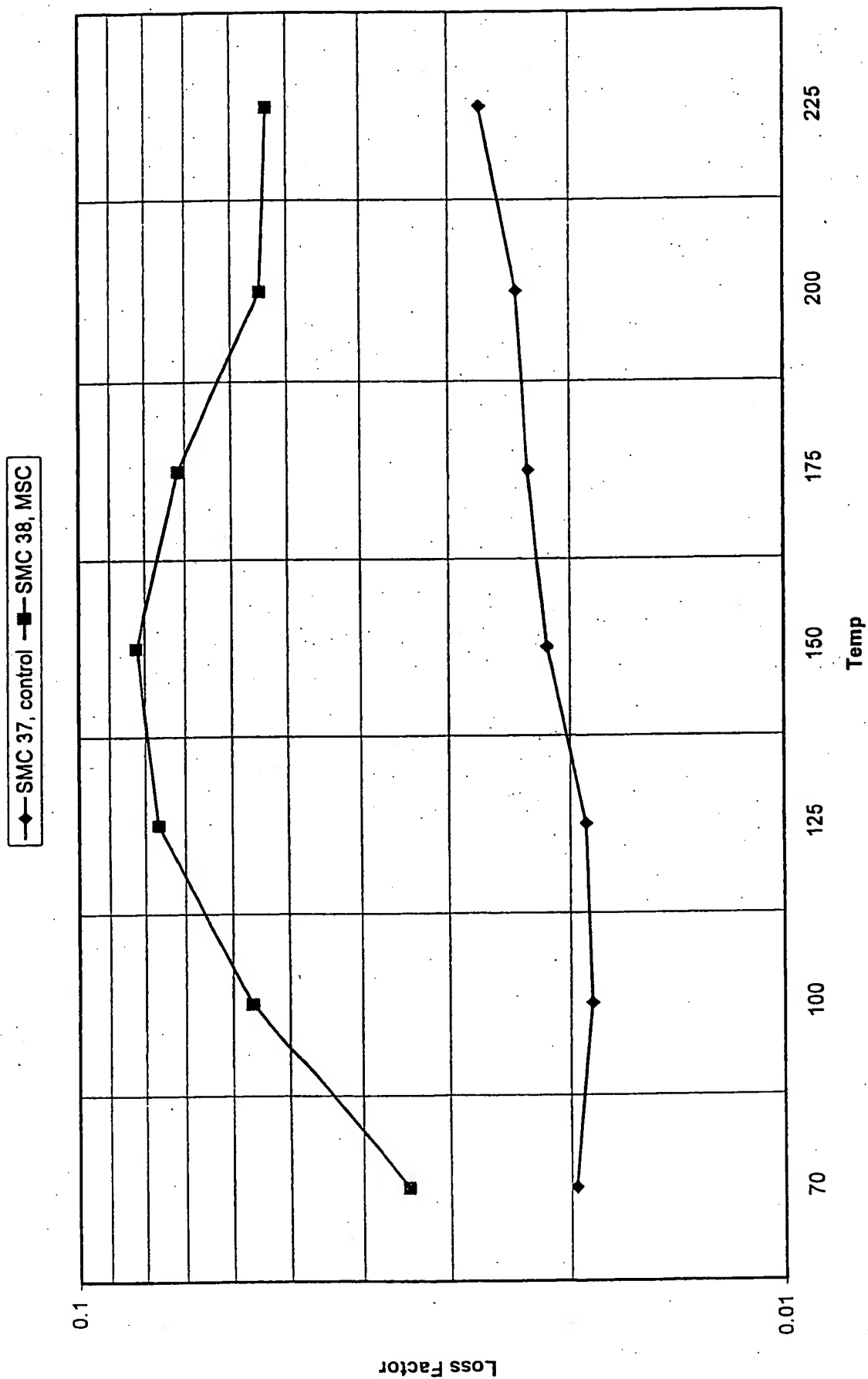


Fig. 8

Jelly rolled SMC, Mode 3

—◆— SMC 37, control —■— SMC 38, MSC

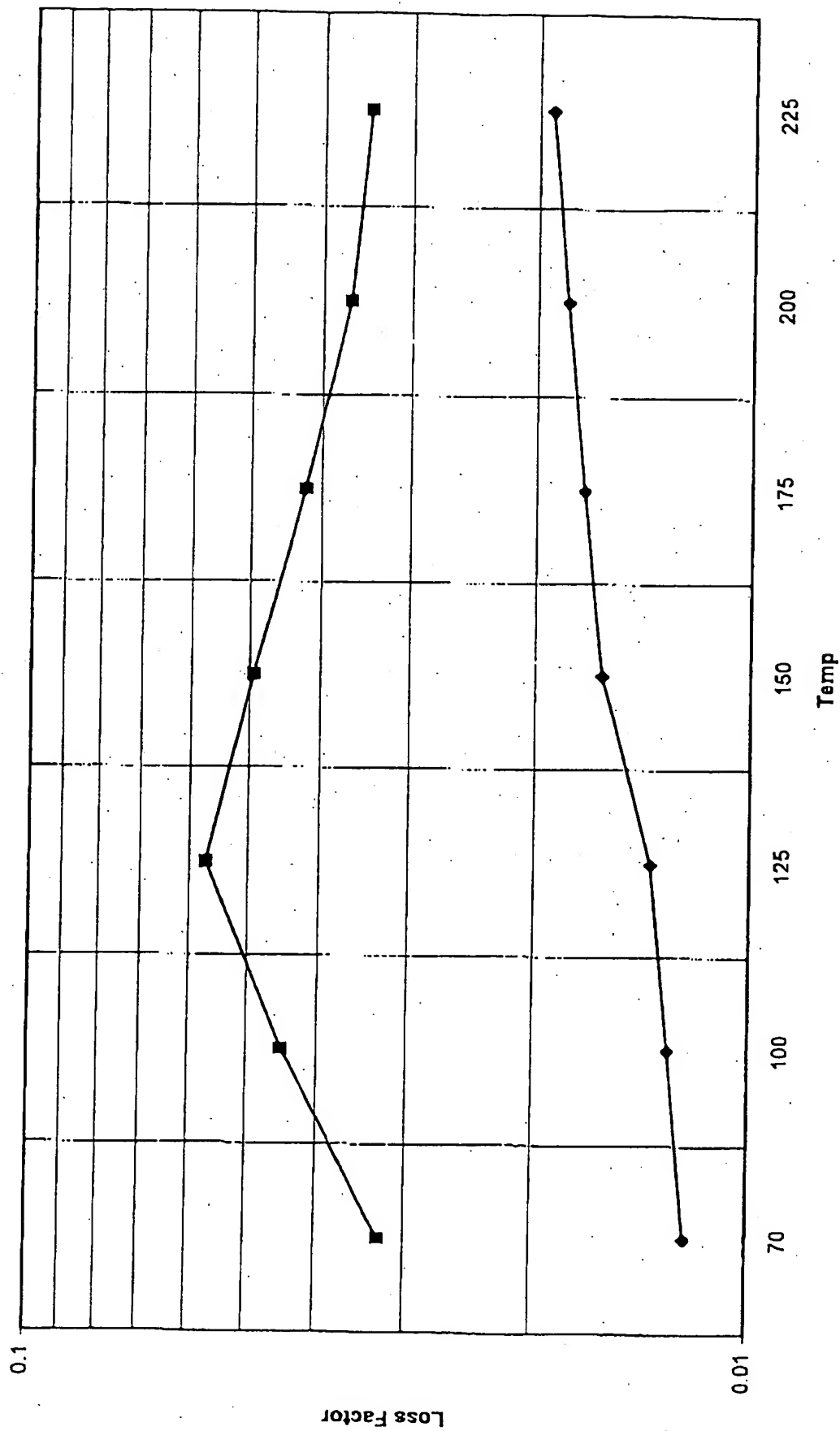


Fig. 9

6 Layer SMC, 1100 Wt., Mode 2

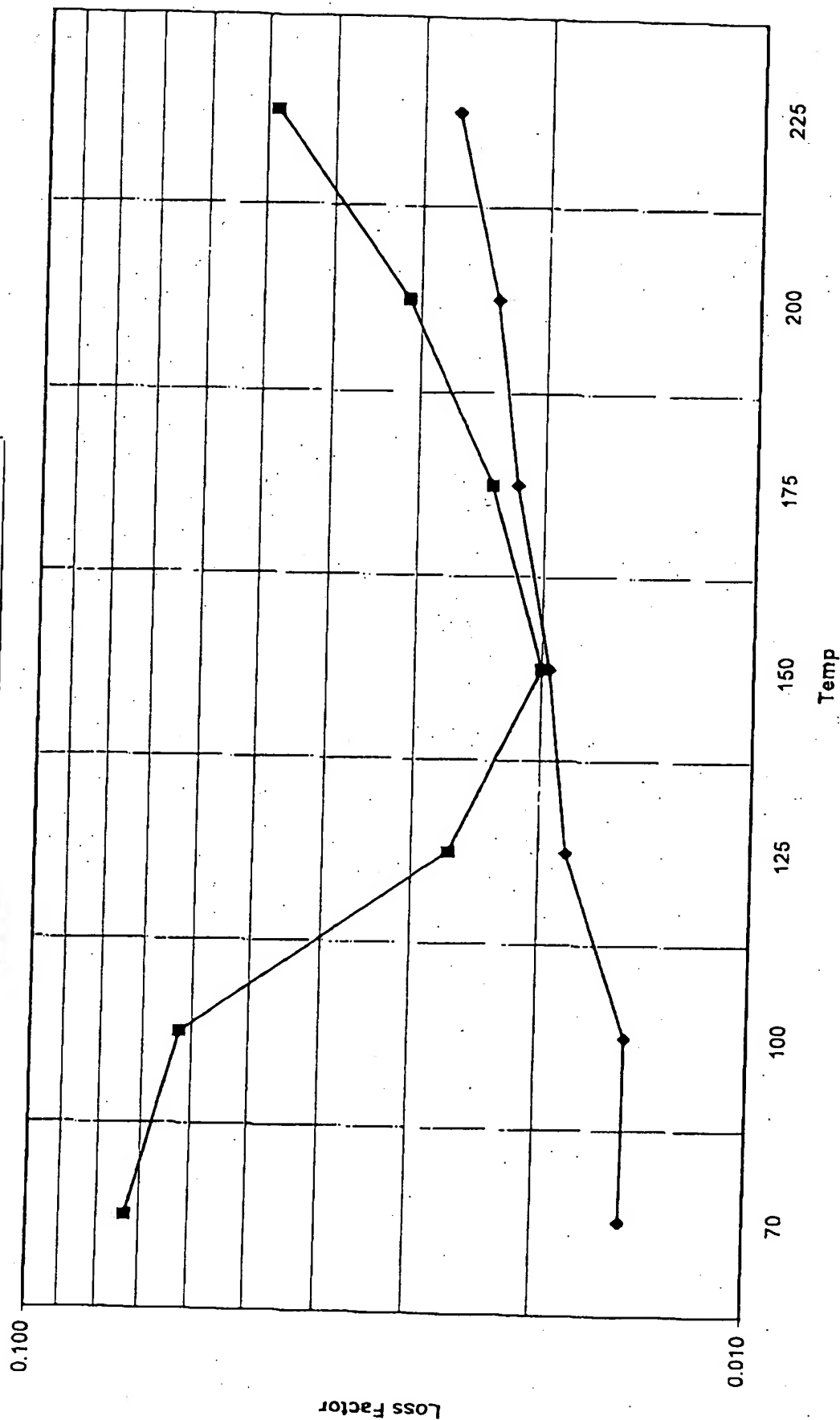
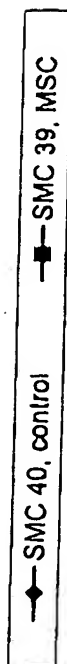


Fig. 10

6 Layer SMC, 1100 Wt., Mode 3

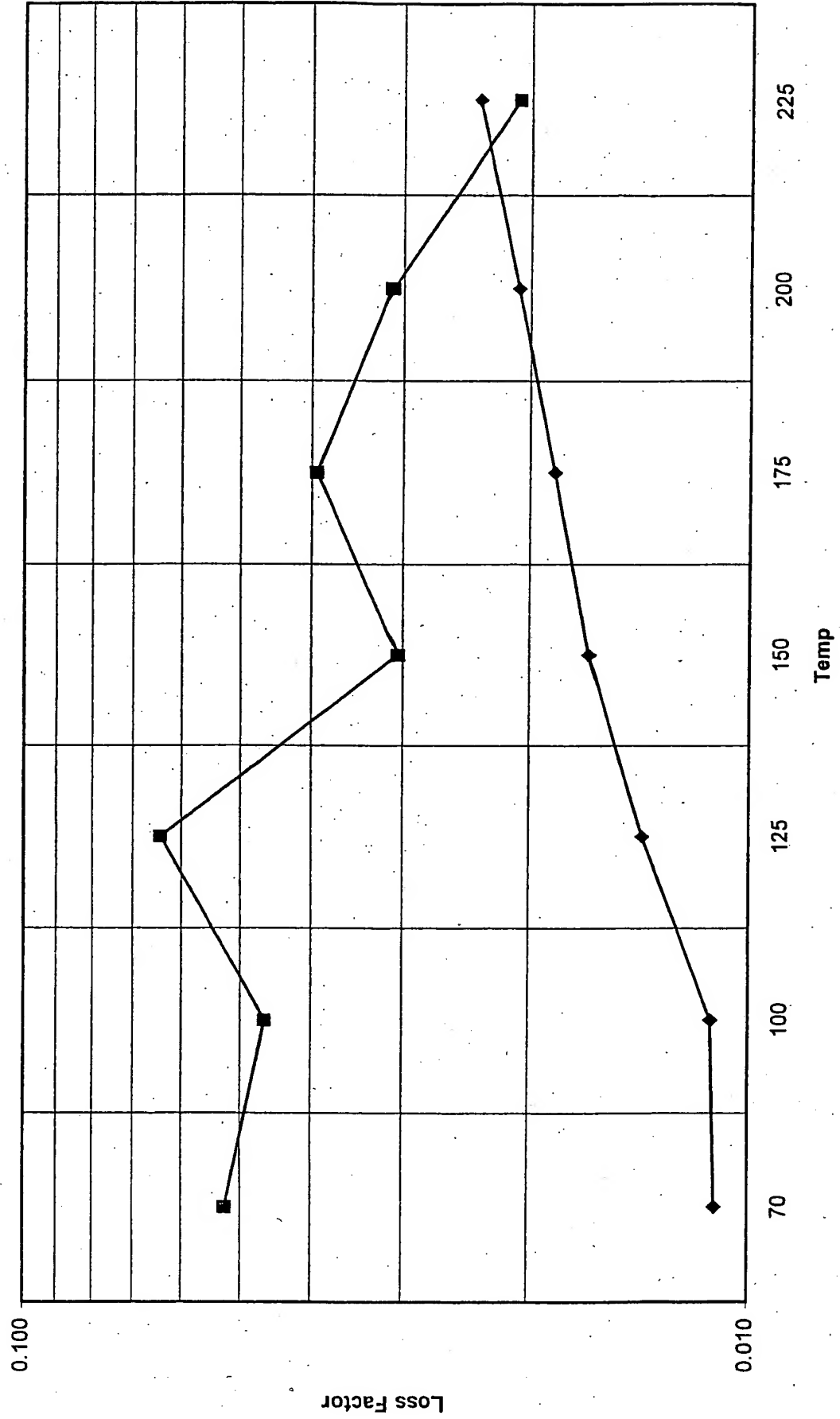
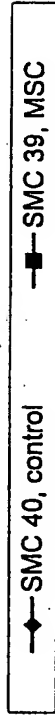


Fig. 11

2 Layer SMC, Mode 2

—◆— SMC 41, MSC —■— SMC 42, control

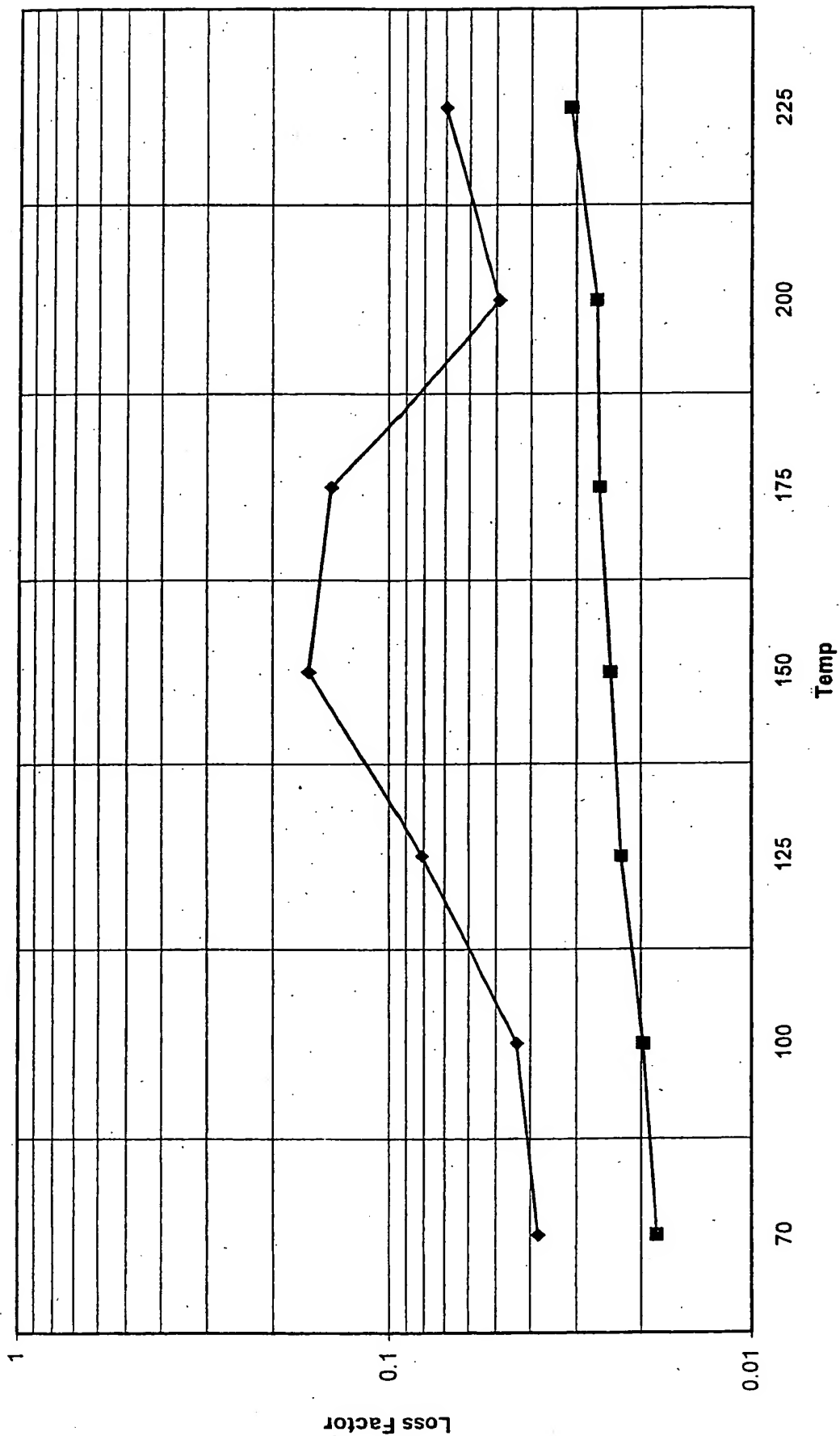


Fig. 12

2 Layer SMC, Mode 3

—◆— SMC 41, MSC —■— SMC 42, control

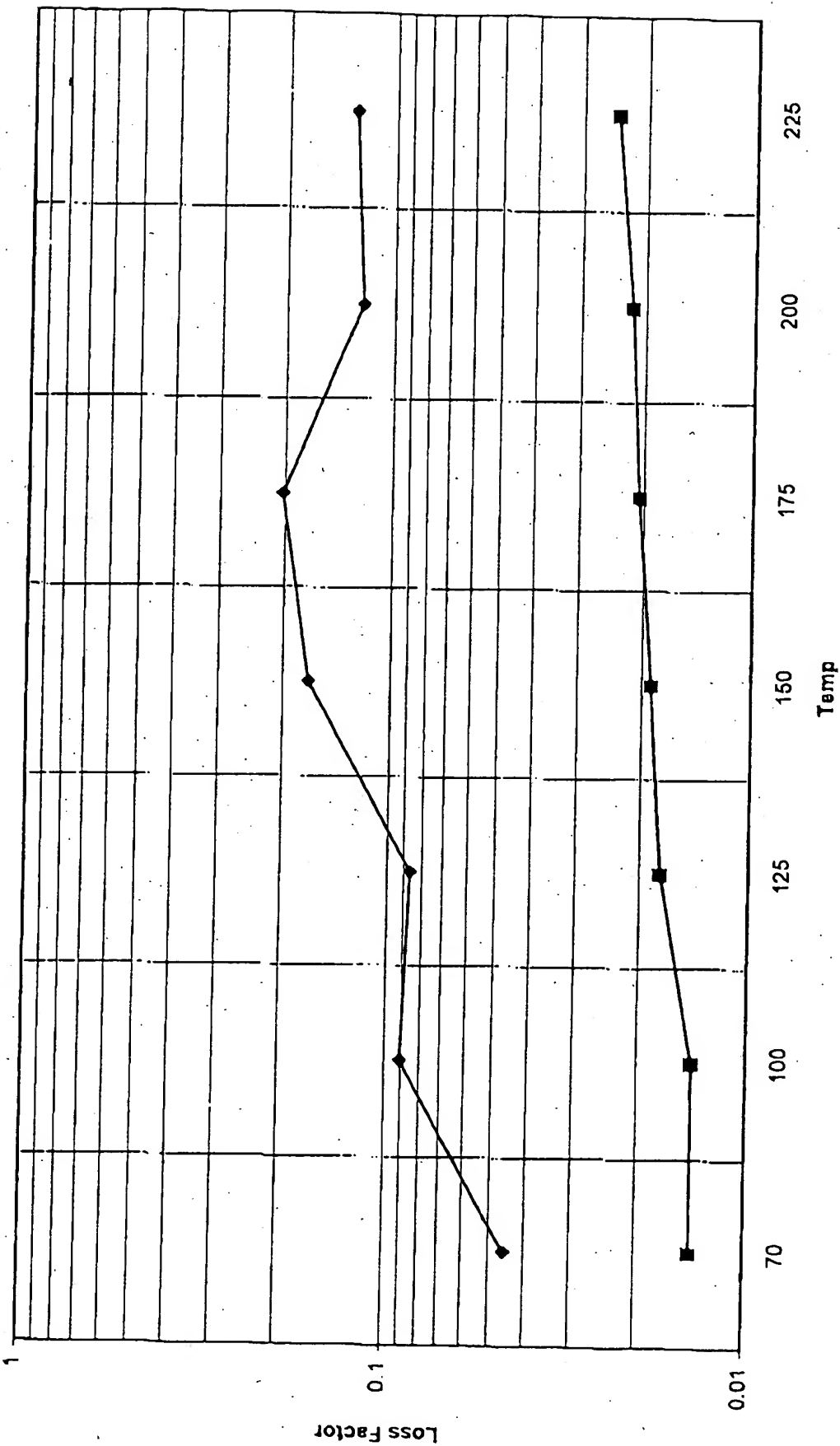


Fig. 13

6 Layer SMC, Mode 2

—◆— SMC 43, control —■— SMC 44, MSC 1 layer —▲— SMC 45, MSC 3 layer

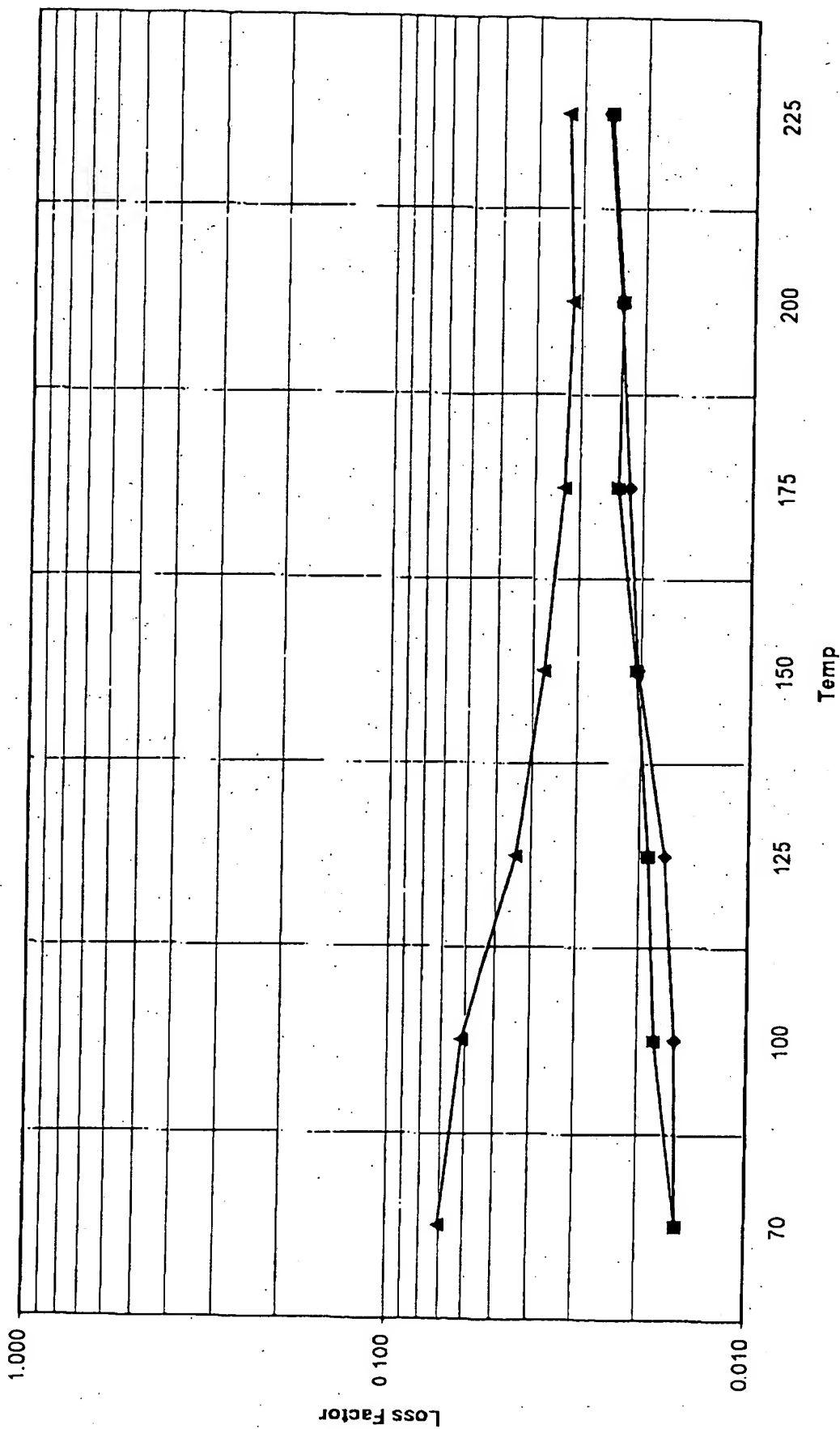


Fig. 14

6 Layer SMC, Mode 3

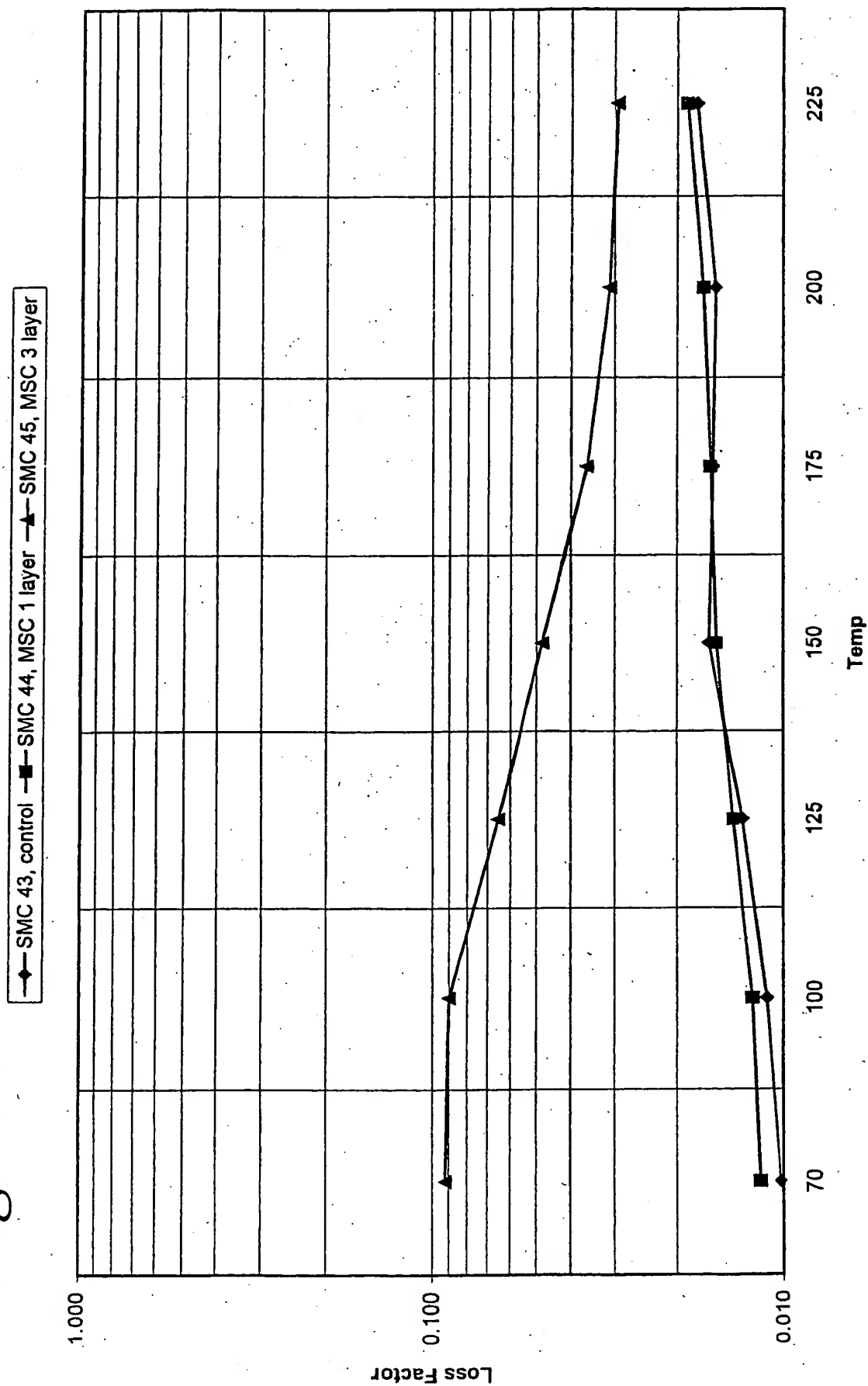


Fig. 15

Phase Epsilon SMC, Mode 2

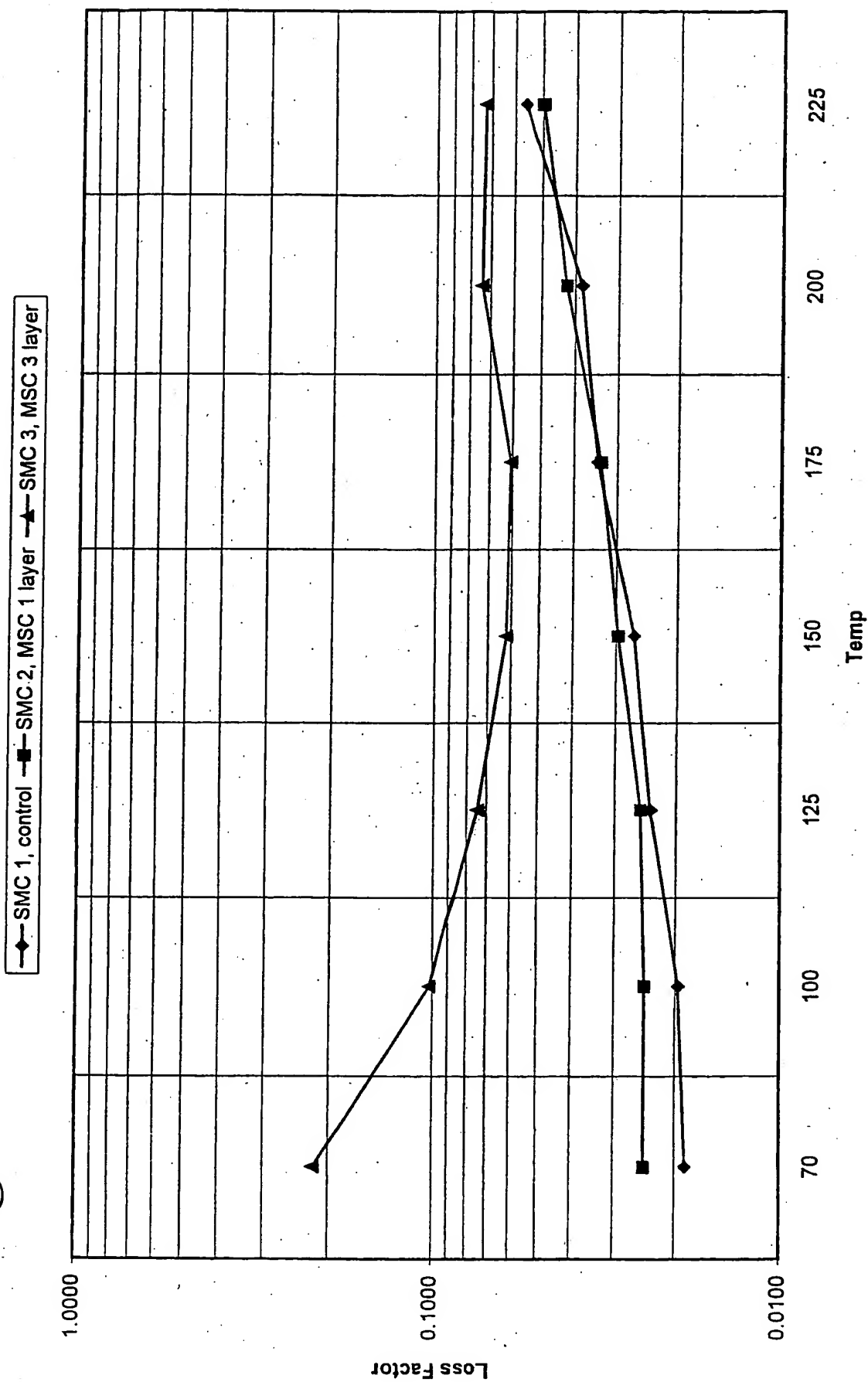
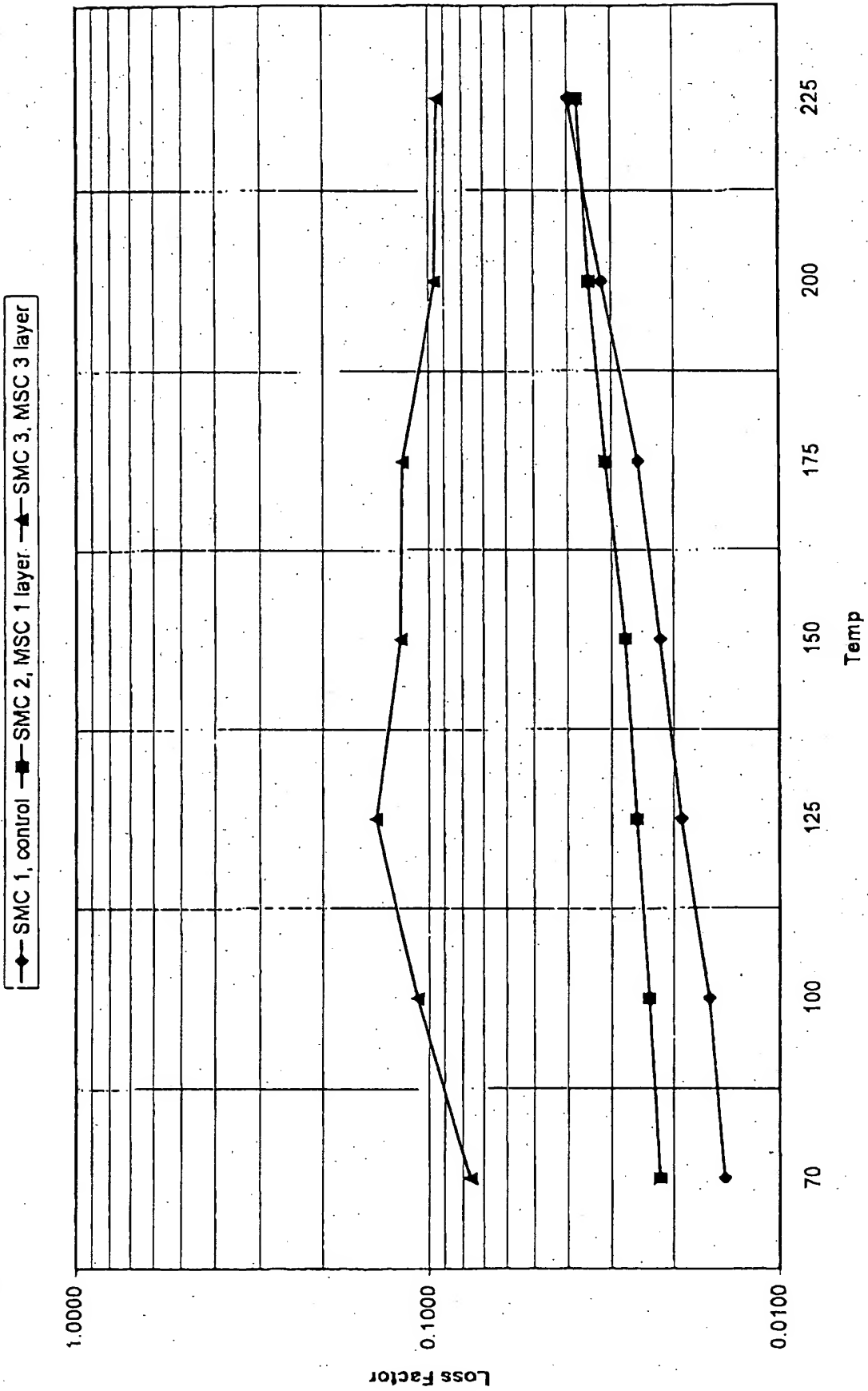


Fig. 16

Phase Epsilon SMC, Mode 3



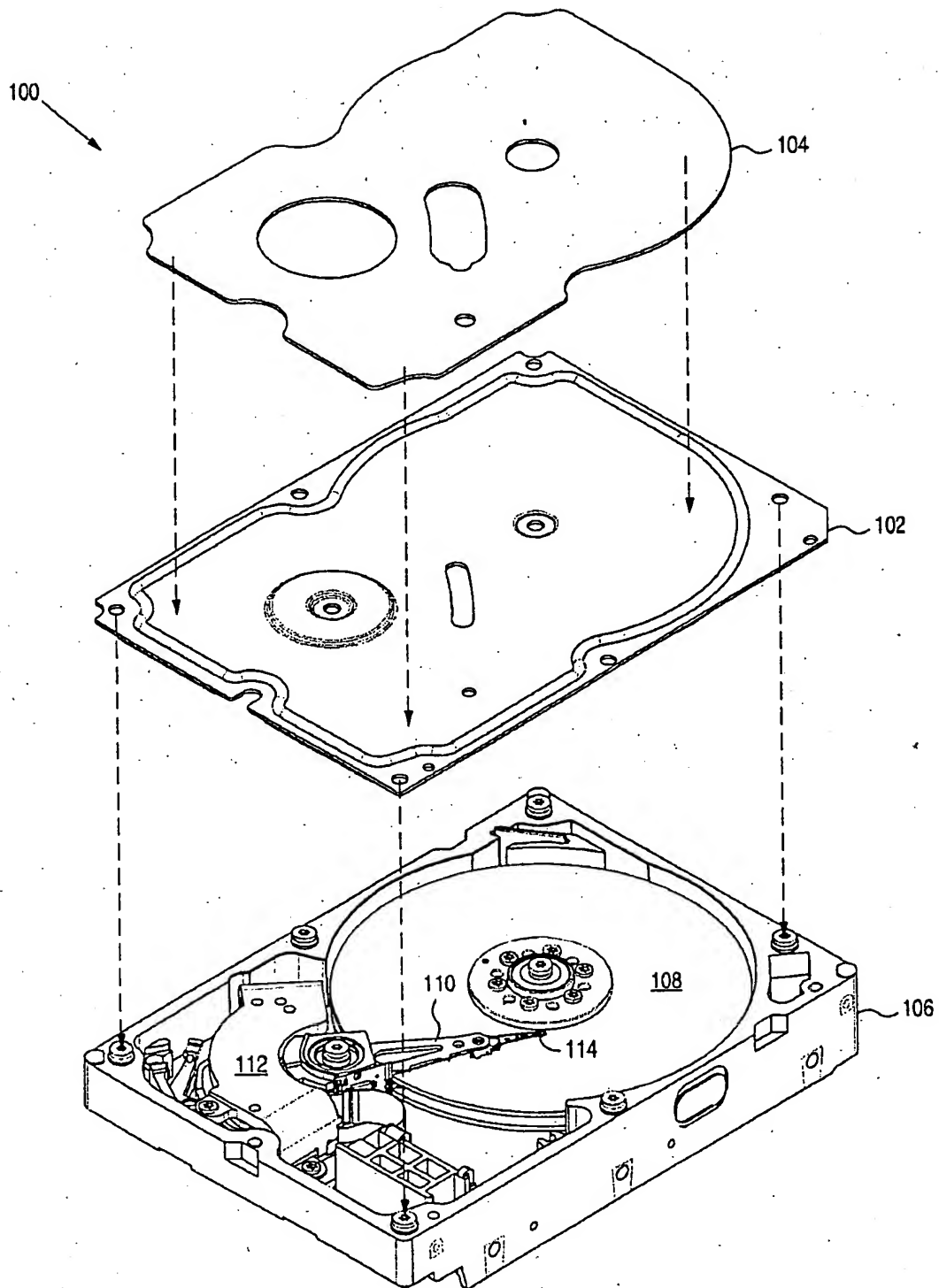


Fig. 17

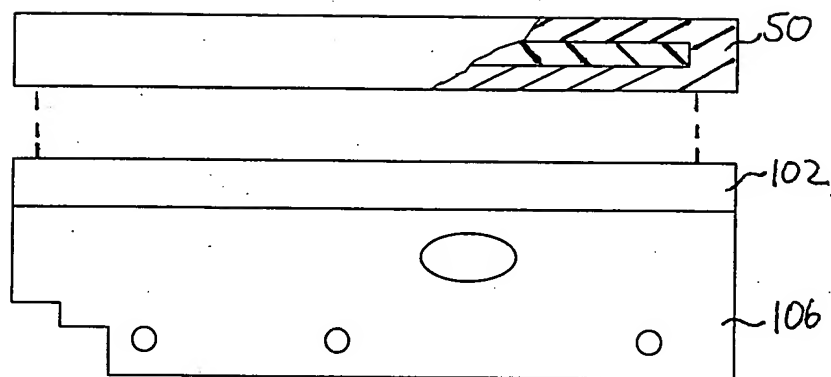


Fig. 18

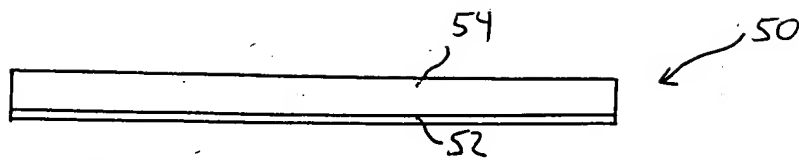


Fig. 19

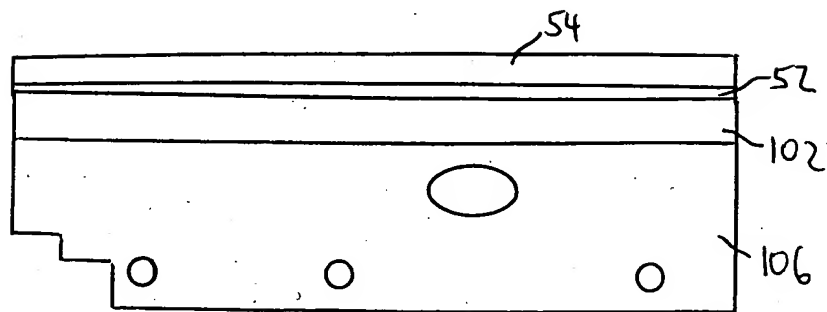


Fig. 20

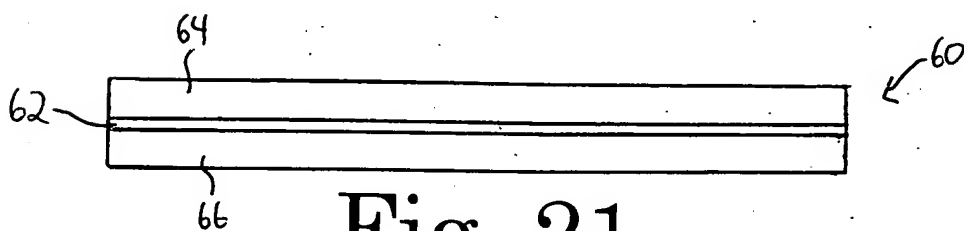


Fig. 21

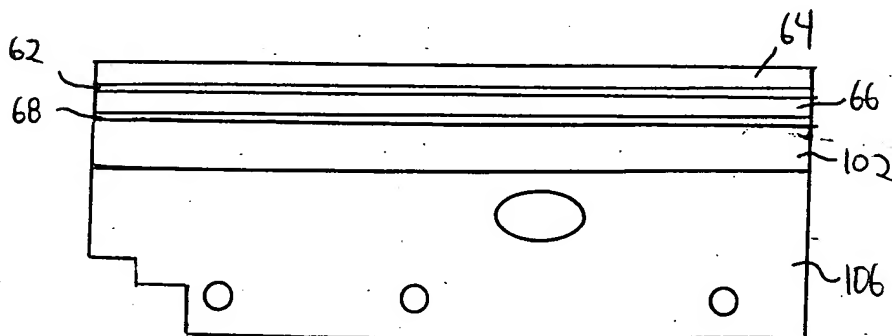


Fig. 22